



SHEET 1 OF 1

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

248183US2

SERIAL NO.

10/767,430

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Tomoaki SHINO

FILING DATE

January 30, 2004

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

ah	AW	T. OHSAWA, et al., International Solid-State Circuits Conference, pages 152, 153 and 454, "MEMORY DESIGN USING ONE-TRANSISTOR GAIN CELL ON SOI", 2002				
	AX					
	AY					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached

Examiner

Date Considered

06/08/05

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.